

# Part Pin-outs

## Lab 2: The Field Effect Transistor

ECE 327: *Electronic Devices and Circuits Laboratory I*

<p><b>CD4066</b> solid-state switch</p>	<p><b>LM741/LF351/CA3160</b> op. amp.</p>
<p><b>CD4049</b> inverter</p>	<p><b>LM555/NE555</b> timer</p>
<p><b>CD4007</b> dual complementary pair/inverter</p> <p>D = DRAIN S = SOURCE</p> <p><math>V_{DD} = \text{PIN } 14 \text{ (HIGH)}</math> <math>V_{SS} = \text{PIN } 7 \text{ (LOW)}</math></p>	